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Applicants: Raymond J. IANNUZZELLI et al. §
Serial No.: 09/981,511 §
Filed: October 17, 2001 §
For: Interposer One-Step Load And §
Self-Lock Socket §

Group Art Unit: 2833
Examiner: E. A. Leon

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RESPONSE TO OFFICE ACTION DATED JUNE 5, 2002

Assistant Commissioner for Patents
Washington, D.C. 20231

Att'y. Docket No. 1662-46500
Client Ref. No. P01-3668
Date: September 5, 2002

Sir:

This paper is filed in response to the Office Action dated June 5, 2002. The Examiner is requested to consider the accompanying remarks. Reconsideration is respectfully requested.

REMARKS

Applicants acknowledge receipt of the Office Action dated June 5, 2002 in which the Examiner rejected claims 1-20 under 35 U.S.C. § 103(a) as being unpatentable over Applicants' alleged admission of prior art in view of *Mori* (U.S. Patent No. 5,318,456).

It is the Examiner's position that Applicants' alleged admission of prior art discloses a component restraint computer system that is used to secure an electronic component (10) to a circuit board (22), comprising a backing plate (14); a spring (18); a post (20) extending from the backing plate (14), the post (20) having a stop surface (16). In addition, it is the Examiner's position that *Mori* discloses a system using a clip (5a, 5b) inserted onto a post (55), being pushed down the post (55) towards a backing plate (24c) until the clip (5a, 5b) engages a stop surface (24a, 24b) of the post (55).